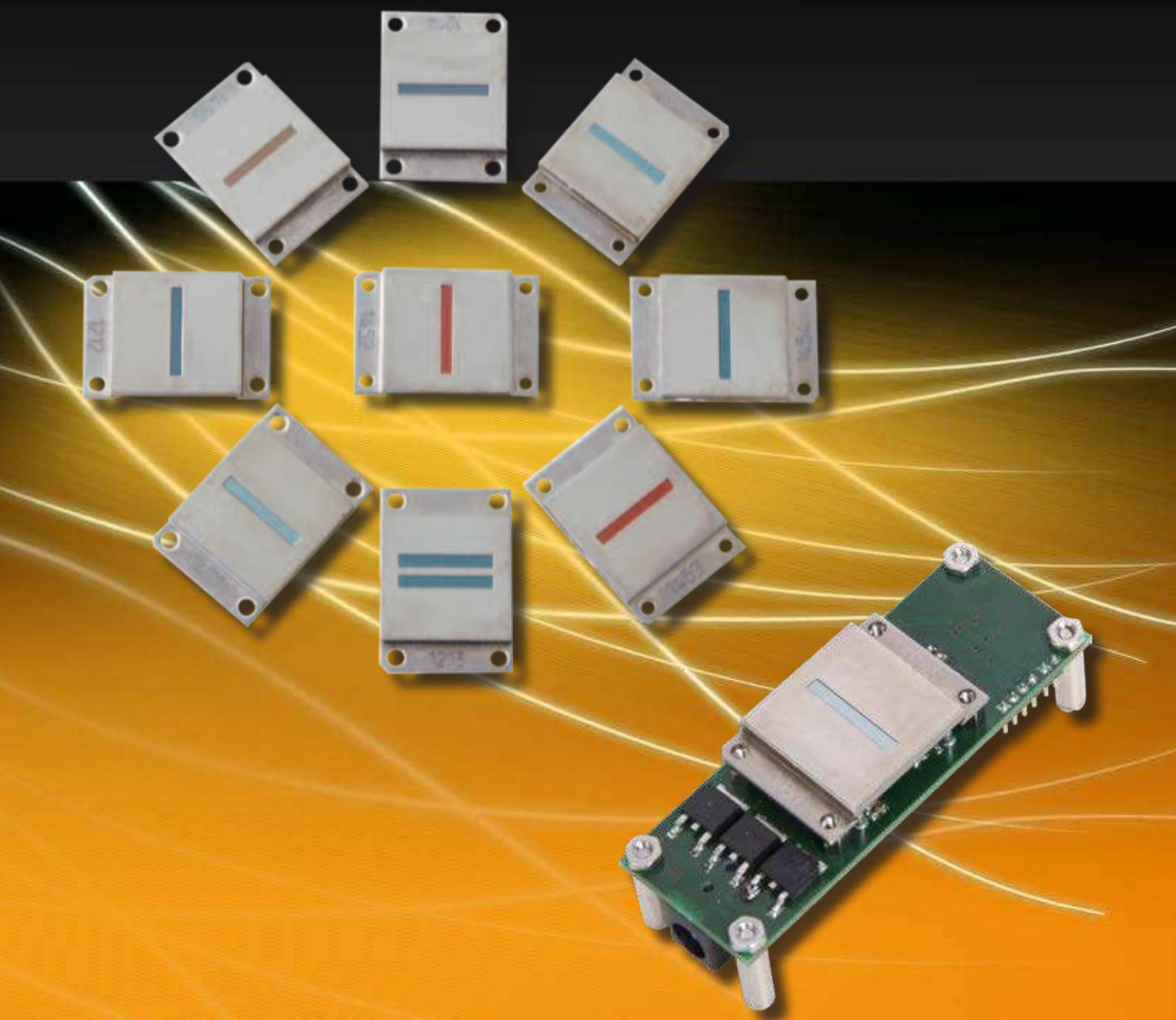


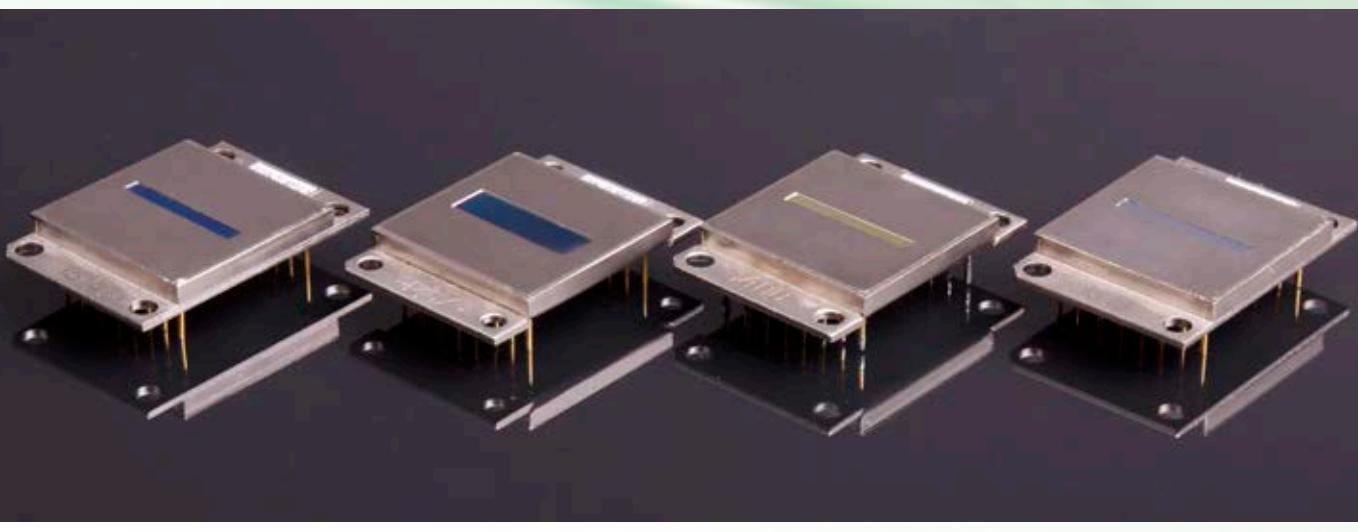
Pyroelectric Linear Arrays **PYROSENS**

For Measurement Applications



PYROSENS – Pyroelectric Linear Arrays 128

with 128, 256 or 510 Elements and Integrated CMOS Multiplexer



Linear Arrays in Volume Production

The LTx family of pyroelectric linear arrays is specifically designed for non-contact temperature measurement and infrared spectrometry. The arrays include a lithium tantalate chip with 128, 256 or 510 elements. The signals produced by the elements are processed in a CMOS circuit. Signal processing is carried out by the analogue circuitry, including an adapted low-noise preamplifier for each pixel, a multiplexer, an output amplifier. The pyroelectric chip and CMOS readout circuit are located on a thick film substrate, which is mounted inside a hermetic metal housing. The incident radiation passes through a window or filter, is transparent to infrared wavelengths, and reaches the sensitive elements.

The preamplifiers transform the signal charges of each pixel into a signal voltage, include bandwidth limiting and pass the amplified signal to the sample & hold for the read-out process. The digital inputs are CMOS compatible.

For measurement of the detector temperature a sensor (type AD 590) is integrated into the package. It provides an output current which is proportional to the temperature.

In common with all pyroelectric detectors, the incoming infrared radiation needs to be modulated for a measurement to be made.

Ion-beam milled pyroelectric detector chips with a thickness of approximately 5 µm enable a high responsivity and a low noise equivalent power NEP. An additional metal black coating („M“ in type designation) realizes a high and homogeneous spectral absorption. Detector chips with ion-beam etched thermal isolation trenches („SL“ in type designation) allow low modulation frequencies up to 10 Hz with excellent signal/noise-ratio.

What does the name of the array say?

Pyroelectrical Linear Arrays – Types and Features										
Type	Sensitive Element				Sensor Parameters ¹					
	Number	Width [µm]	Length [µm]	Pitch [µm]	f _{ch} in Hz	S _v in 10 ³ [V/W]	u _R [mV]	NEP [nW]	MTF (R = 3 lp/mm)	Uniformity of S _v [%]
Element length 0.1 mm										
128LT	128	90	100	100	128	230	0.7	3.0	0.6	5
128LTI	128	90	100	100	128	540	0.8	1.5	0.6	10
128LTMI	128	90	100	100	128	620	0.8	1.3	0.6	8
128LTMI SL	128	90	100	100	10	8000	0.8	0.1	0.8	10
128LTMI SL	128	90	100	100	128	620	0.8	1.3	0.8	10
256LTI	256	42	100	50	128	620	0.7	1.1	0.6	10
256LTMI	256	42	100	50	128	710	0.7	1.0	0.6	8
256LTMI SL	256	42	100	50	10	9100	0.7	0.08	0.8	10
256LTMI SL	256	42	100	50	128	710	0.7	1.0	0.8	10
510LTI	510	20	100	25	128	680	0.9	1.3	0.8	10
Element length 0.5 mm										
128LT SP0.5	128	90	500	100	128	230	0.9	3.9	0.6	10
128LTI SP0.5	128	90	500	100	128	540	1.2	2.2	0.6	10
128LTMI SP0.5	128	90	500	100	128	620	1.2	1.9	0.6	8
128LTMI SP0.5 V3	128	90	500	100	128	1900	3.2	1.7	0.6	8
128LTMI SL SP0.5	128	90	500	100	10	8000	1.2	0.15	0.8	10
128LTMI SL SP0.5	128	90	500	100	128	620	1.2	1.9	0.8	10
128LTMI SL SP0.5 V3	128	90	500	100	10	24000	3.2	0.13	0.8	10
128LTMI SL SP0.5 V3	128	90	500	100	128	1900	3.2	1.7	0.8	10
256LTI SP0.5	256	42	500	50	128	620	0.9	1.4	0.6	10
256LTMI SP0.5	256	42	500	50	128	710	0.9	1.2	0.6	8
256LTMI SL SP0.5	256	42	500	50	10	9100	0.9	0.10	0.8	10
256LTMI SL SP0.5	256	42	500	50	128	710	0.9	1.2	0.8	10
256LTI SP0.5 V3	256	42	500	50	128	1850	2.2	1.2	0.6	10
256LTMI SP0.5 V3	256	42	500	50	128	2100	2.2	1.0	0.6	8
256LTMI SL SP0.5 V3	256	42	500	50	10	27000	2.2	0.08	0.8	10
256LTMI SL SP0.5 V3	256	42	500	50	128	2100	2.2	1.0	0.8	10
256LTI SP0.5 V5	256	42	500	50	128	3100	4.5	1.4	0.6	10
256LTI SP0.5 V8	256	42	500	50	128	4950	6.5	1.3	0.6	10
256LTI SP0.5 V16	256	42	500	50	128	9900	13.0	1.3	0.6	10
510LTI SP0.5	510	20	500	25	128	680	1.3	1.7	0.8	10
Element length 1.0 mm										
128LT SP1.0	128	90	1000	100	128	230	1.1	4.9	0.6	10
128LTI SP1.0	128	90	1000	100	128	540	1.9	3.5	0.6	10
128LTMI SP1.0	128	90	1000	100	128	620	1.9	3.0	0.6	8
256LTI SP1.0	256	42	1000	50	128	620	1.1	1.8	0.6	10
256LTMI SP1.0	256	42	1000	50	128	710	1.1	1.6	0.6	8
256LTI SP1.0 V3	256	42	1000	50	128	1850	3.0	1.6	0.6	10
256LTI SP1.0 V5	256	42	1000	50	128	3100	5.6	1.8	0.6	10
256LTI SP1.0 V8	256	42	1000	50	128	4950	8.5	1.7	0.6	10
256LTI SP1.0 V16	256	42	1000	50	128	9900	17.0	1.7	0.6	10
256LTMI SP1.0 V3	256	42	1000	50	128	2100	3.0	1.4	0.6	8

¹ Typical values, rectangular chopping with f_{ch}, array temperature 25 °C, black body source temperature 400 °C, filter transmission 100 %.

f_{ch} ... Chopper frequency for rectangular modulation

S_v ... Sensitivity

u_R ... Noise voltage

NEP ... Noise equivalent power

MTF ... Modulation transfer function

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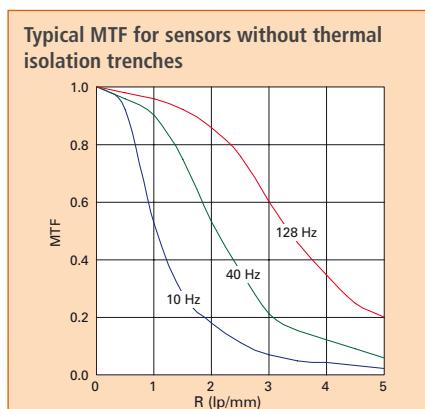
Pyroelectric Linear Arrays – Technical Data

Technical Data

Features	Maximum/minimum Conditions ¹	Typical Responsivity
<ul style="list-style-type: none"> – 128, 256 or 510 pixel arranged in one line – NEP (128 Hz) down to 1.1 nW (128LTx, 256LTx), 1.3 nW (510LTx) – Dynamic range > 75 dB – Modulation frequency up to 512 Hz – Output voltage $2.5\text{ V} \pm 2\text{ V}$ – Integrated CMOS multiplexer – High long-term stability – Simple mode of operation – Operation at ambient temperature – Small package – Coated silicon or germanium as infrared window – Broad band windows ($> 1.3\text{ }\mu\text{m}$) or special filters on request – Customized arrays with up to 510 elements with special sizes 	<ul style="list-style-type: none"> – VDD, VD2: -0.3 V to 7 V – Digital inputs CLK, RES, VVR, VDR, VSH: -0.3 V to $\text{VDD} + 0.3\text{ V}$ – Chopping frequency f_{ch}: 10 Hz to 512 Hz – AD590+ to AD590-: -20 V to 44 V – Analog output²: $\pm 5\text{ mA}$ – Maximum irradiance: 50 mW/mm^2 – Soldering temperature: $300\text{ }^\circ\text{C}$ – Storage temperature: $-20\text{ }^\circ\text{C}$ to $80\text{ }^\circ\text{C}$ – Operation temperature: $-15\text{ }^\circ\text{C}$ to $70\text{ }^\circ\text{C}$ 	

¹ All voltages refer to ground (pin 10, 15).

² Not short resistant.



Electrical Parameters³

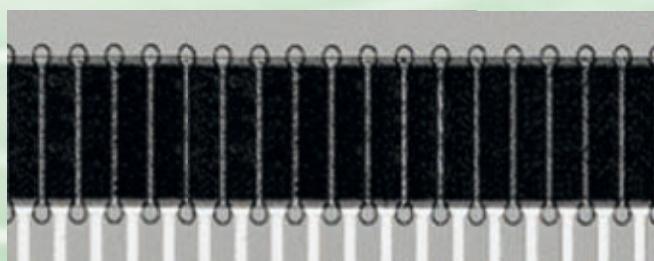
Parameter	Minimum value	Typical value	Maximum value	Unit
VDD	4.75	5.0	5.25	V
VD2	2.4	2.5	2.6	V
Digital inputs, low voltage	0		0.3 VDD	V
Digital inputs, high voltage	0.7 VDD		VDD	V
Digital inputs, switching threshold		0.5 VDD		V
Digital inputs, leakage current			± 1	μA
Current consumption		8		mA
AD590 operating voltage ⁴	4 ⁵		30	V

³ All values for VDD = 5 V, VD2 = 2.5 V. ⁴ See data sheet of Analog Devices. ⁵ Valid for 510LTx.

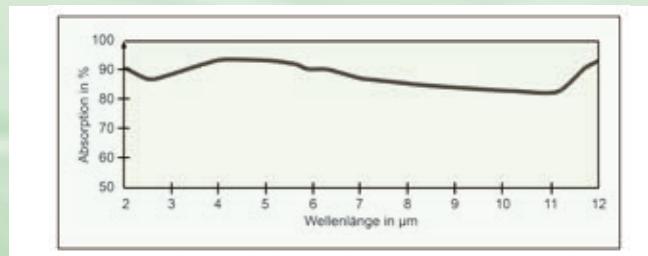
Pins – 128LTx, 256LTx and 510LTx

Pin	Name	Function	Pin	Name	Function
1	CLK	Input clock CLK (trigger on rising edge)	9	OUT, OUT1 ⁶	Analog signal output, analog signal output (odd pixels) ⁶
2	RES	Input clock RES (active low)	10	GND	Ground
3	VVR	Input clock VVR (active high)	11	n.c., OUT2 ⁶	Not connected, analog signal output (even pixels) ⁶
4	VDR	Input clock VDR (active high)	12	AD590+	Temperature sensor
5	VSH	Input clock VSH (active high)	13	AD590-	Temperature sensor
6	VD2	Operating voltage (+2.5 V)	14	case	Case
7	VDD	Operating voltage (+5 V)	15	GND	Ground
8	VD2	Operating voltage (+2.5 V)	16	VDD	Operating voltage (+5 V)

⁶ Only available for 510LTx.

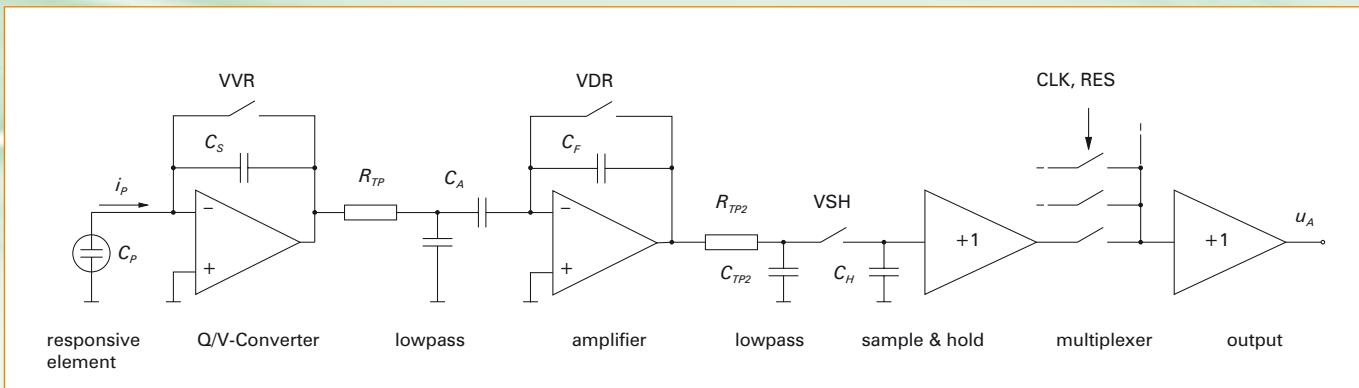


Detail view of a pyroelectric lithium tantalat chip with an additional metal black coating and ion-beam etched isolation trenches.



Typical wavelength dependence of the spectral absorption of the optimal metal absorber.

Internal Read-out Circuit

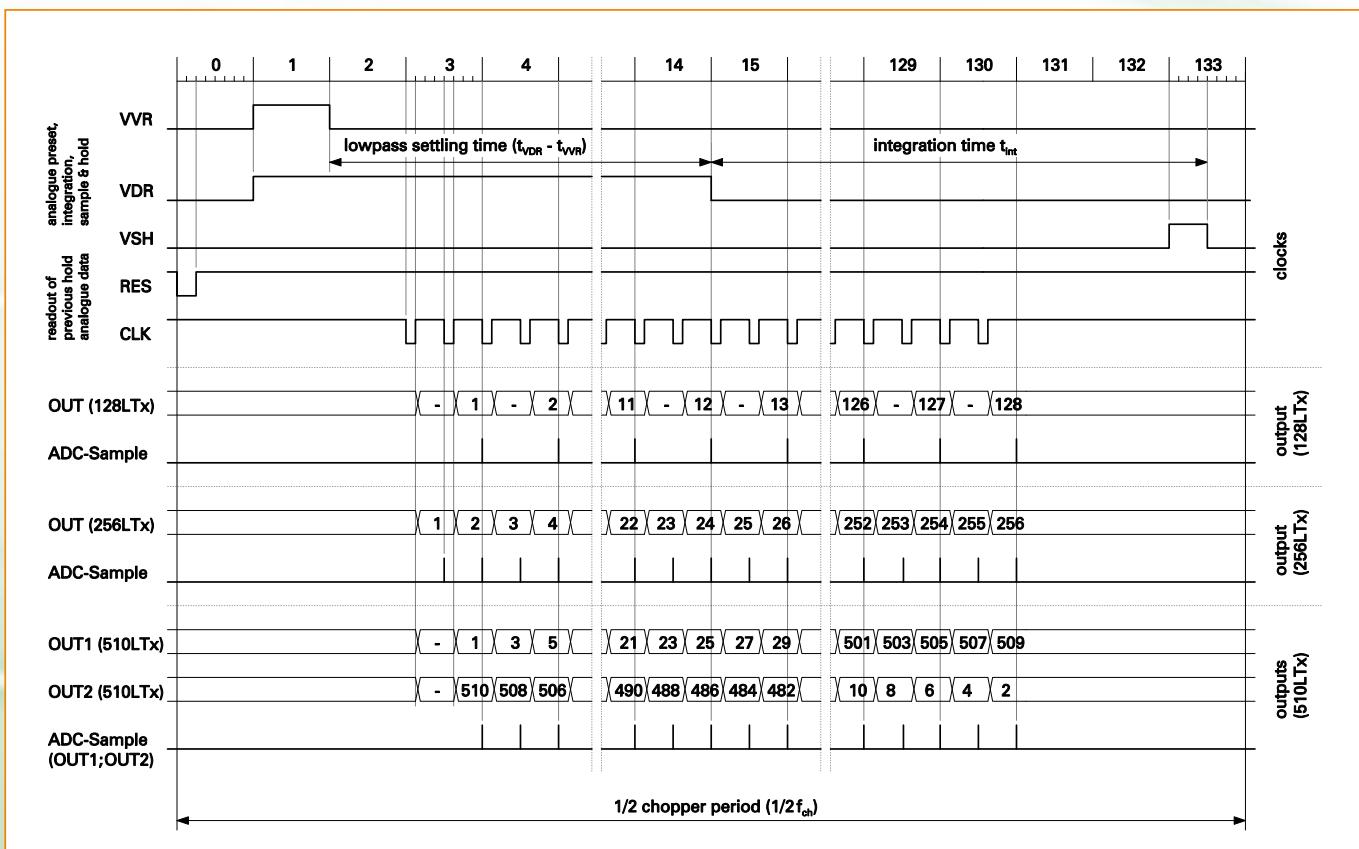


Clock parameters¹

Parameter	Relative value	Minimum value	Typical value	Maximum value	Unit
Chopping frequency ² f_{ch}		10	128	512	Hz
Readout CLK $f_{clk} = 2 \cdot f_{ch} \cdot 268$	$1/t_{clk}$	0	69	300	kHz
Reset clock low-impulse duration t_{res}	$1/2 t_{clk}$	1.8	7.5		μs
Clock VVR high-impulse duration t_{vvr}	$2 t_{clk}$	7.5	30		μs
Clock VDR high-impulse duration ³ t_{vdr}	$28 t_{clk}$	200	400		μs
Clock VSH high-impulse duration t_{vsh}	$1 t_{clk}$	3.5	15		μs
Setting time at the output t_{out}				1	μs

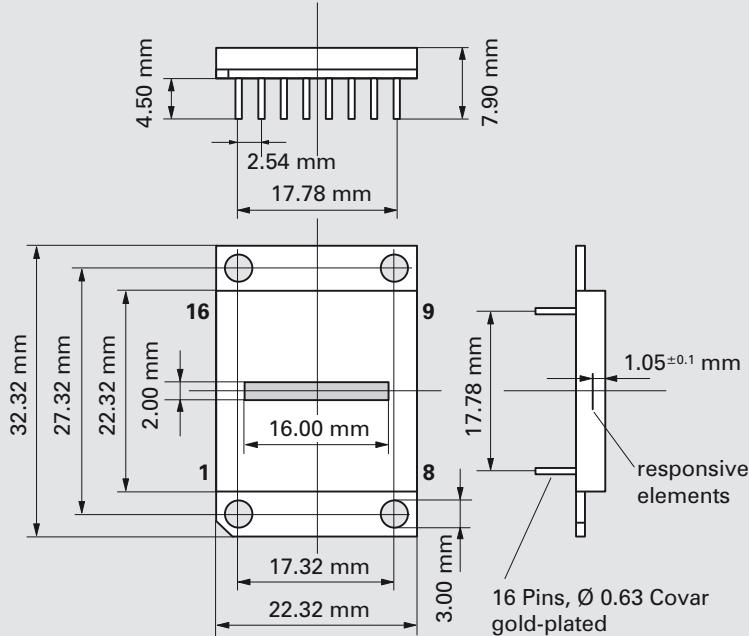
¹All values for VDD = 5 V, VD2 = 2.5 V. ² $t_{ch\ low} = t_{ch\ high}$. ³ For $f_{ch} = 512$ Hz must be $t_{vdr} = 56 \cdot t_{clk} = 200 \mu s$.

Clock diagram

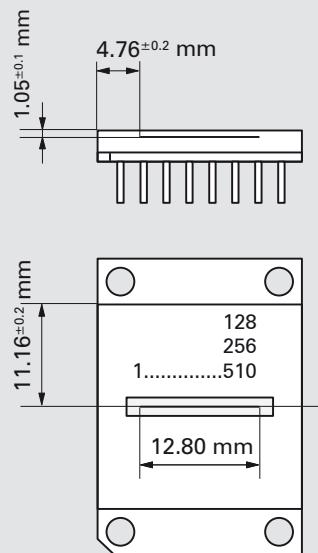


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Dimensional drawings



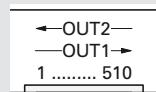
Connect pin 6 to pin 8 (VD2), pin 7 to pin 16 (VDD), pin 10 to pin 15 (GND).



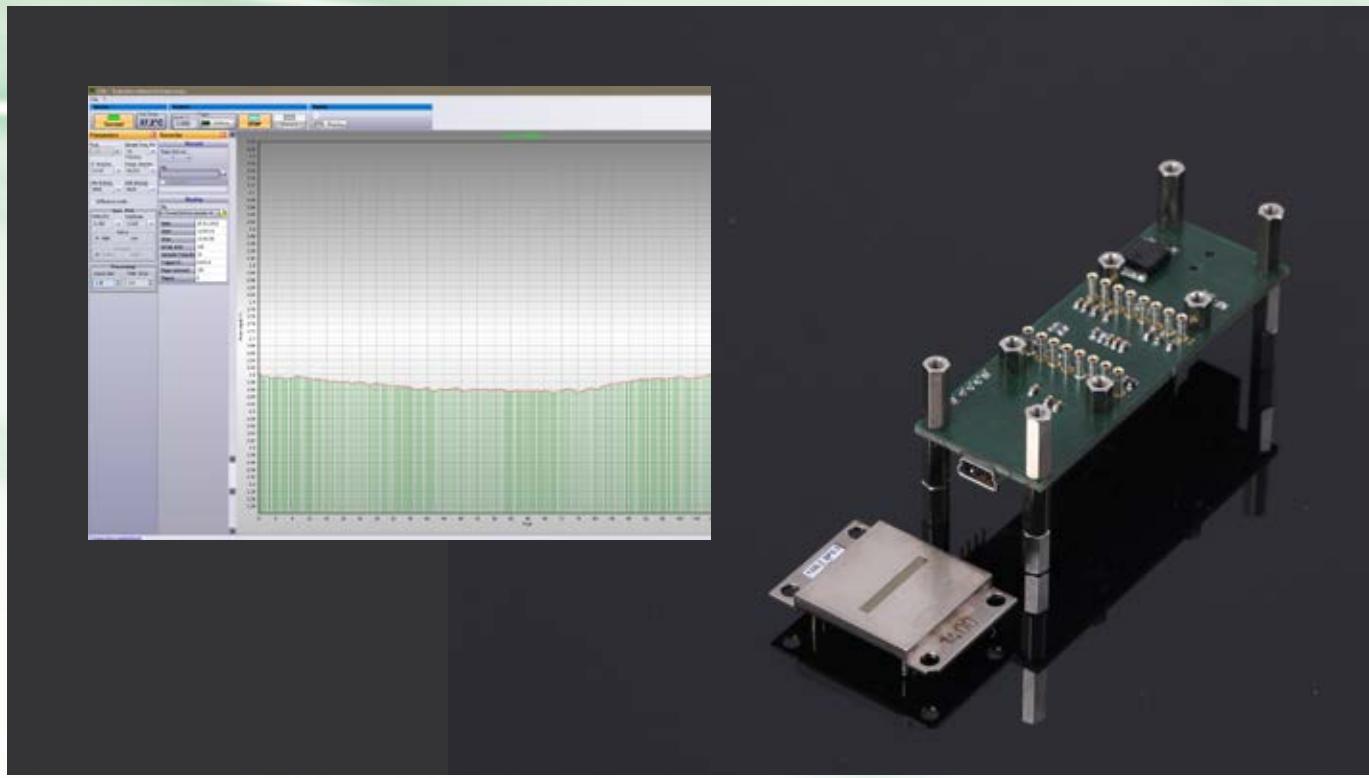
Readout direction (510LTx only):

OUT1 (odd pixel): 1,3,...509

OUT2 (even pixel): 510, 508,...2



Evaluation Kit



The **Evaluation Kit** allows easy operation of the PYROSENS arrays. It consists of a small circuit board with complete electronics and software by which the electronics is controlled via USB connection from a Windows PC. The power can be provided by the USB port or a separate power supply (9 V). For synchronisation with further external components, such as for radiation modulation, a trigger pulse is provided. The read-out cycle can be adjusted between 1 and 30 lines/s.

A DLL interface for the integration of the evaluation kit in custom software solutions and common laboratory software is available. Thus, commissioning and integration of PYROSENS arrays in their own software and system solutions become even easier. The interface permits access to all array parameters via API functions and reading out the pixel values.

The evaluation kit can be integrated into a variety of software environments, e.g.

- NATIONAL INSTRUMENTS LabVIEW
- MathWorks MATLAB
- Microsoft Visual Studio
- Embarcadero RAD Studio

For the development of software in C / C ++ corresponding header and lib files are included.

